

JAN 14 2003 12:38PM

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NO. 5986 P. 1/4

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From: Erwin J. Basinski**Date: January 14, 2003**

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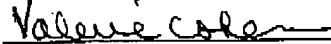
Comments:

Re: U.S. Patent Application Serial No. 09/936,032
Title: **LOGIC CIRCUIT PROTECTED AGAINST TRANSIENT
DISTURBANCES**
By: Michael NICOLAIDIS
Our reference: 51484-2000100

PATENT
Docket No. 514842000100

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on January 14, 2003.


Valerie Cohen

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Michael NICOLAIDIS

Serial No.: 09/936,032

Filing Date: March 11, 2002

For: LOGIC CIRCUIT PROTECTED
AGAINST TRANSIENT
DISTURBANCES

Examiner: To Be Assigned

Group Art Unit: 2816

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REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Please furnish the undersigned with a Corrected Filing Receipt for the above-identified application. The corrections needed are as follows. Please correct the title from "LOGIC CIRCUIT PROTECTED AGAINST TRANSITORY PERTURBATIONS" to the correct title as follows -- **LOGIC CIRCUIT PROTECTED AGAINST TRANSIENT DISTURBANCES** --.

A copy of the marked-up Filing Receipt is attached.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for

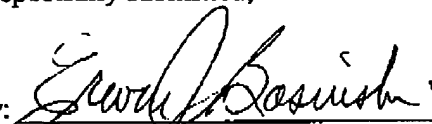
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any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing 514842000100. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: January 14, 2003

Respectfully submitted,

By:


Erwin J. Basinski
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NO. 5986 P. 4/4-2



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/936,032	03/11/2002	2816	600	514842000100	7	10	4

CONFIRMATION NO. 9437

Morrison & Foerster
425 Market Street
San Francisco, CA 94105-2482

FILING RECEIPT



"OC000000008182968"

Date Mailed: 05/31/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Michael Nicolaidis, Saint Egreve, FRANCE;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/FR00/00573 03/08/2000

Foreign Applications

FRANCE 99/03027 03/09/1999

Projected Publication Date: Not Applicable, filed prior to November 29, 2000

Non-Publication Request: No

Early Publication Request: No

** SMALL ENTITY **

Title

LOGIC CIRCUIT PROTECTED AGAINST TRANSIENT DISTURBANCES
~~logic circuit protected against transitory perturbations~~

Preliminary Class

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